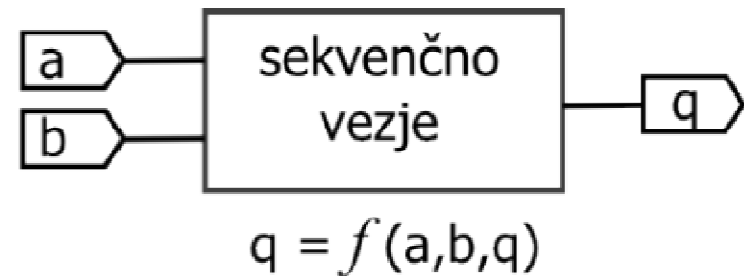
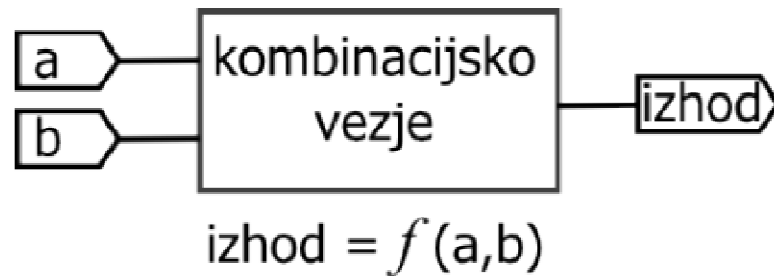


Sekvenčna vezja

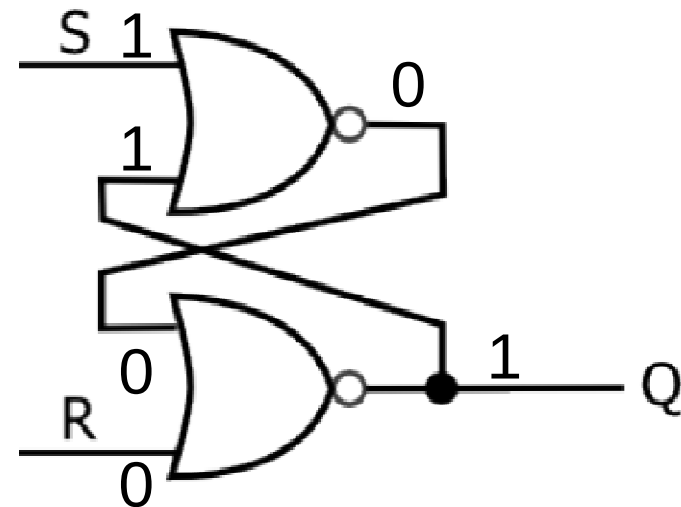


Zapah

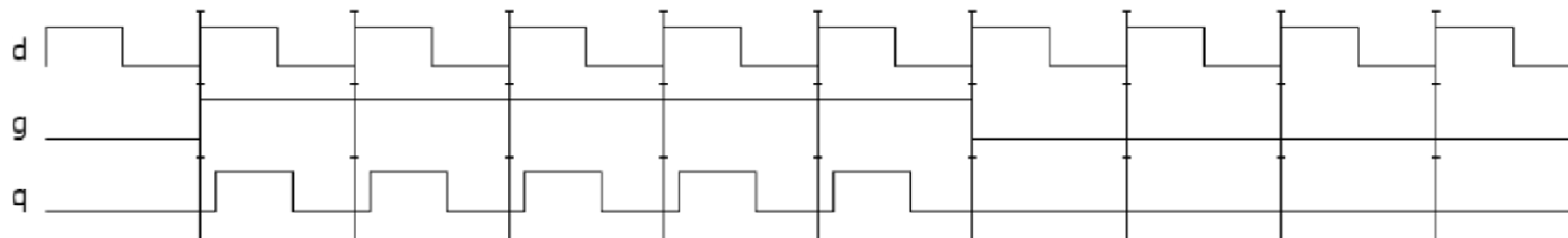
S=1, Q=1

R=1, Q=0

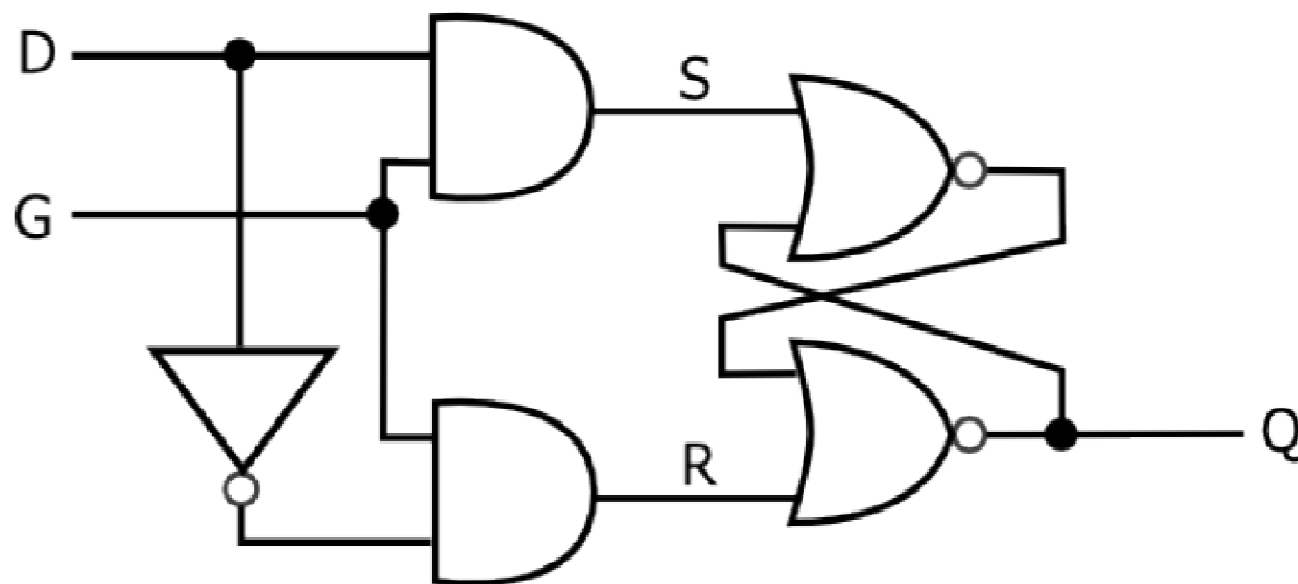
S=0, R=0, Q=Q



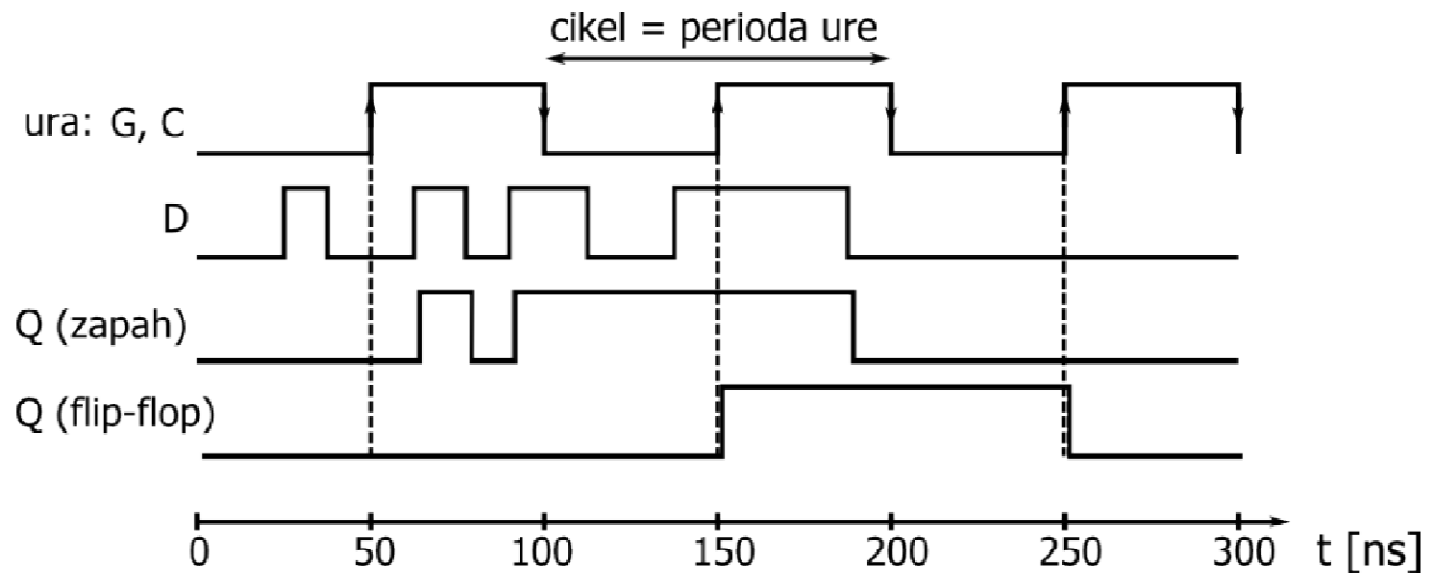
Zapah D



G=1, zapah je transparenten

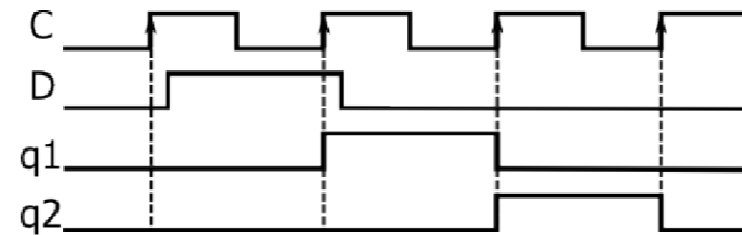
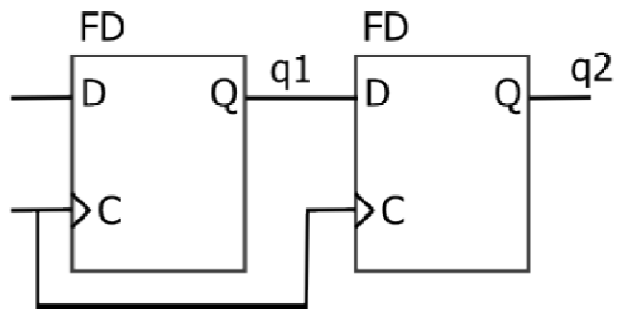
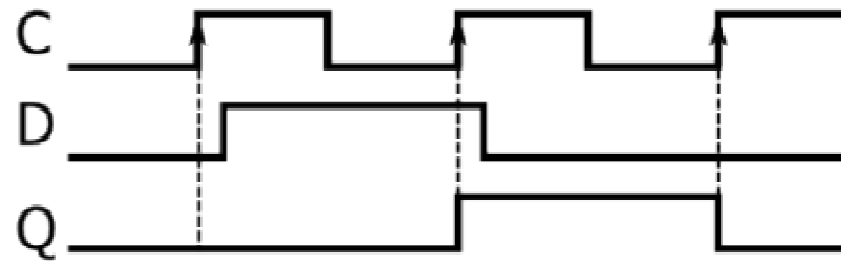
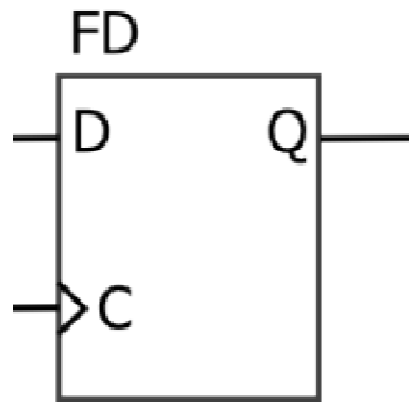


Ura in flip-flop

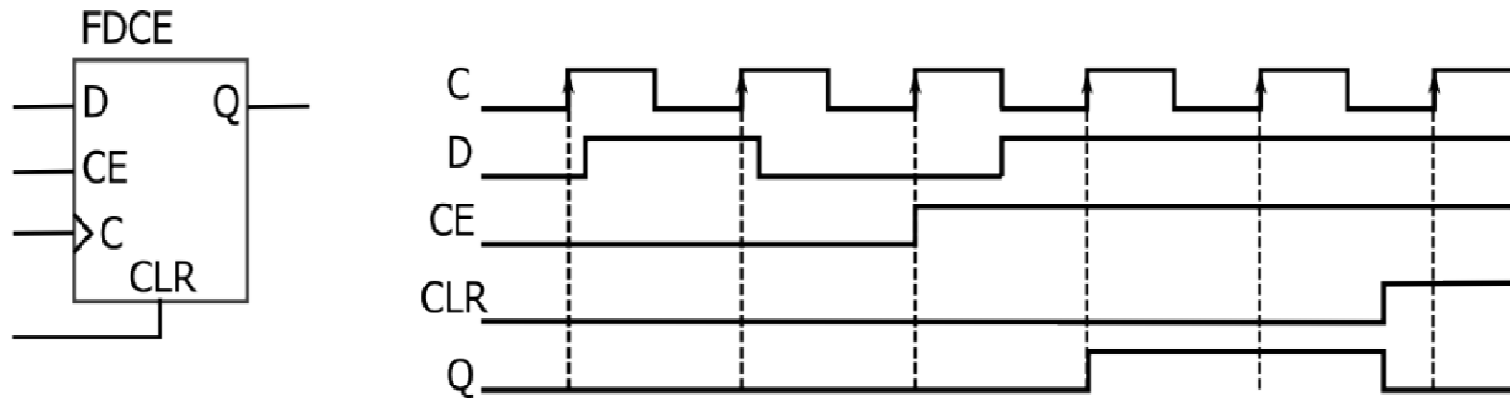


Flip-flop vzorči vhod (spremeni izhod) ob fronti ure

Flip-flop D



Flip-flop, omogoči in reset



CE = Clock Enable, omogoči spremembo izhoda

CLR = Clear, postavi izhod na 0

Register in števec

